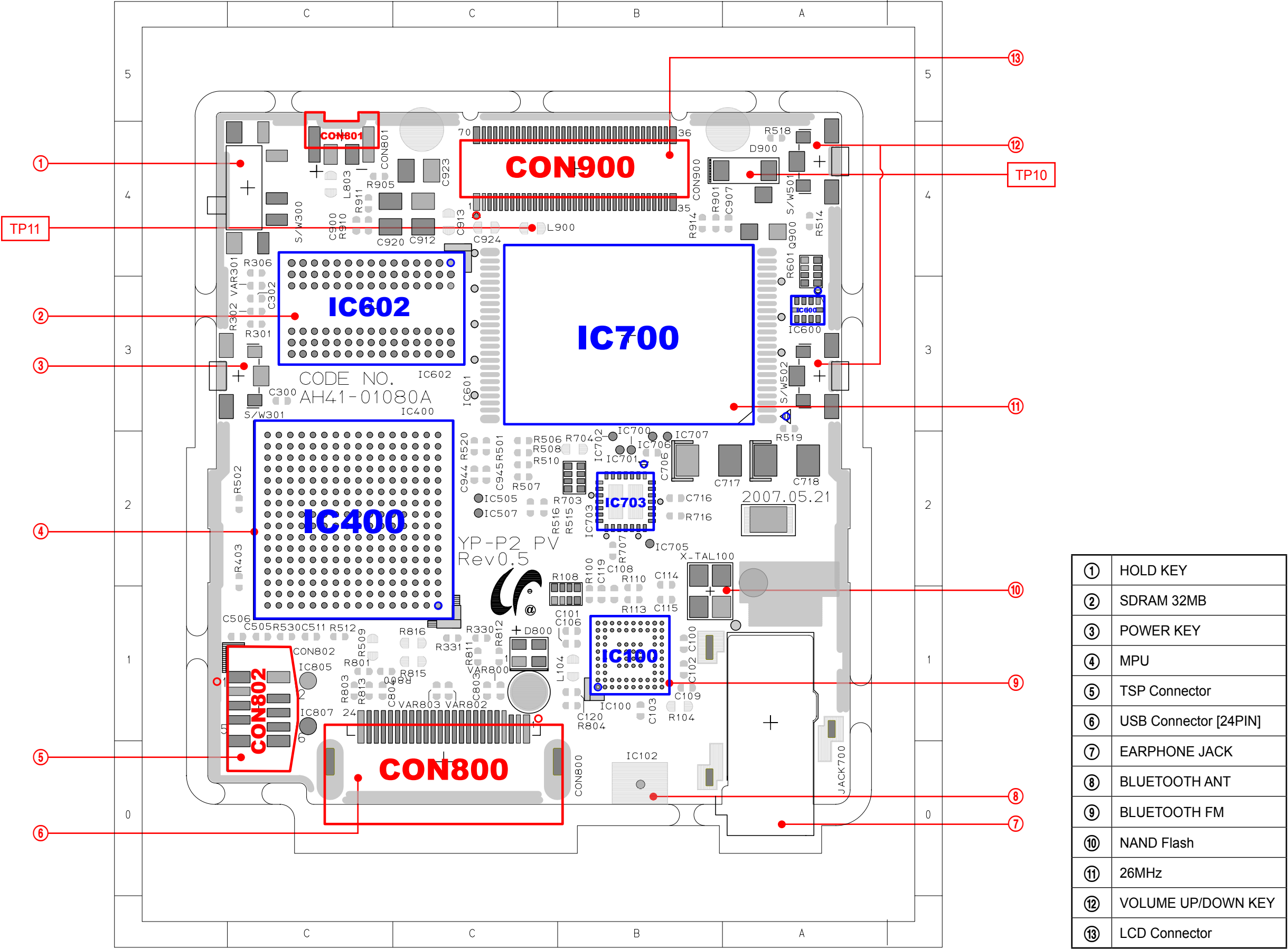


6. PCB Diagram

6-1 PCB Top



6-1-1 Pin Connection

⑤ TSP Connector

Pin No.	Signal
1	GND
2	CLEAR_DAT
3	CLEAR_CLK
4	P2.8V
5	CLEAR_EXTRA
6	CLEAR_ATT

⑦ EARPHONE JACK

Pin No.	Signal
1	GROUND
2	R-CH
3	L-CH

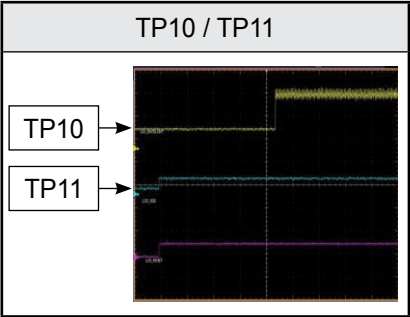
⑥ USB Connector

Pin No.	Signal	Pin No.	Signal
1	BAT ID	13	UART RX1
2	DEVICE_DET	14	URAT TX1
3	DC2.8V OUT	15	USB D+
4	ADAPTOR (DC5V)	16	USB PWR (DC5V)
5	ADAPTOR (DC5V)	17	LINE_OUT_R
6	DEBUG_PWR_ON	18	LINE_OUT_L
7	CRADLE_DET	19	GND
8	TEST POINT	20	CRADLE_FM_ANT
9	TEST POINT	21	BATT (DC4.2V)
10	USB D-	22	BATT (DC4.2V)
11	N.C	23	EXT_BAT_DET
12	GND	24	PBA_CE

⑬ LCD Connector

Pin No.	Signal	Pin No.	Signal	Pin No.	Signal
1	VDD2	39	VLED-	56	VCL
2	VDDA	40	VLED+	57	VLOUT3
3	GND	41	N.C	58	V7
4	GND	42	CS	59	V8
5	VDD	43	SDA	60	V9
6	VDD	44	SCL	61	V10
7	VDDIO	45	VSYNC	62	V11
8	VDD_25V	46	HSYNC	63	V12
9-16	RED [0~7]	47	GRB	64	VLOUT2
17	GND	48	DCLK	65	VGH
18-25	GREEN [0~7]	49	V1	66	VHL
26	GND	50	V2	67	VCOMH
27-34	BLUE [0~7]	51	V3	68	VCOML
35	GND	52	V4	69	FRP
36	VCOM	53	V5	70	VCOM
37	DRV	54	V6		
38	N.C	55	VDD3		

6-1-2 Test Point Wave Form



6-2-1 Pin Connection

⑦ Debugging Connector

Pin No.	Signal
1	P2.8V
2	JTAG_RTCK
3	JTAG_TCK
4	JTAG_TDO
5	JTAG_TMS
6	JTAG_TDI
7	#JTAG_RST
8	GND
9	UART_TX1
10	UART_RX1
11	DEBUG_PWR_ON
12	V_DDCON

6-2-2 Test Point Wave Form

