

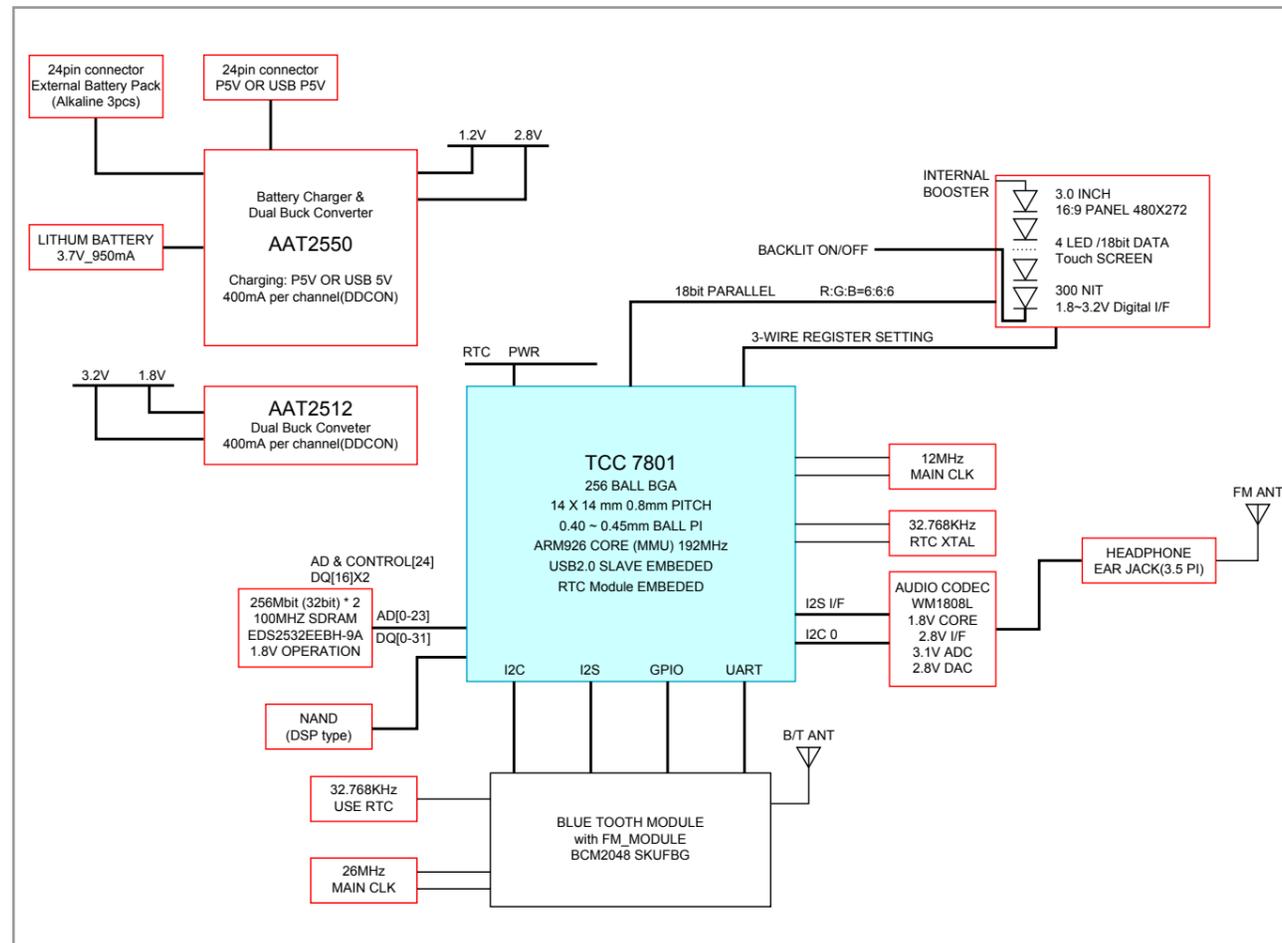
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## 7. Schematic Diagram

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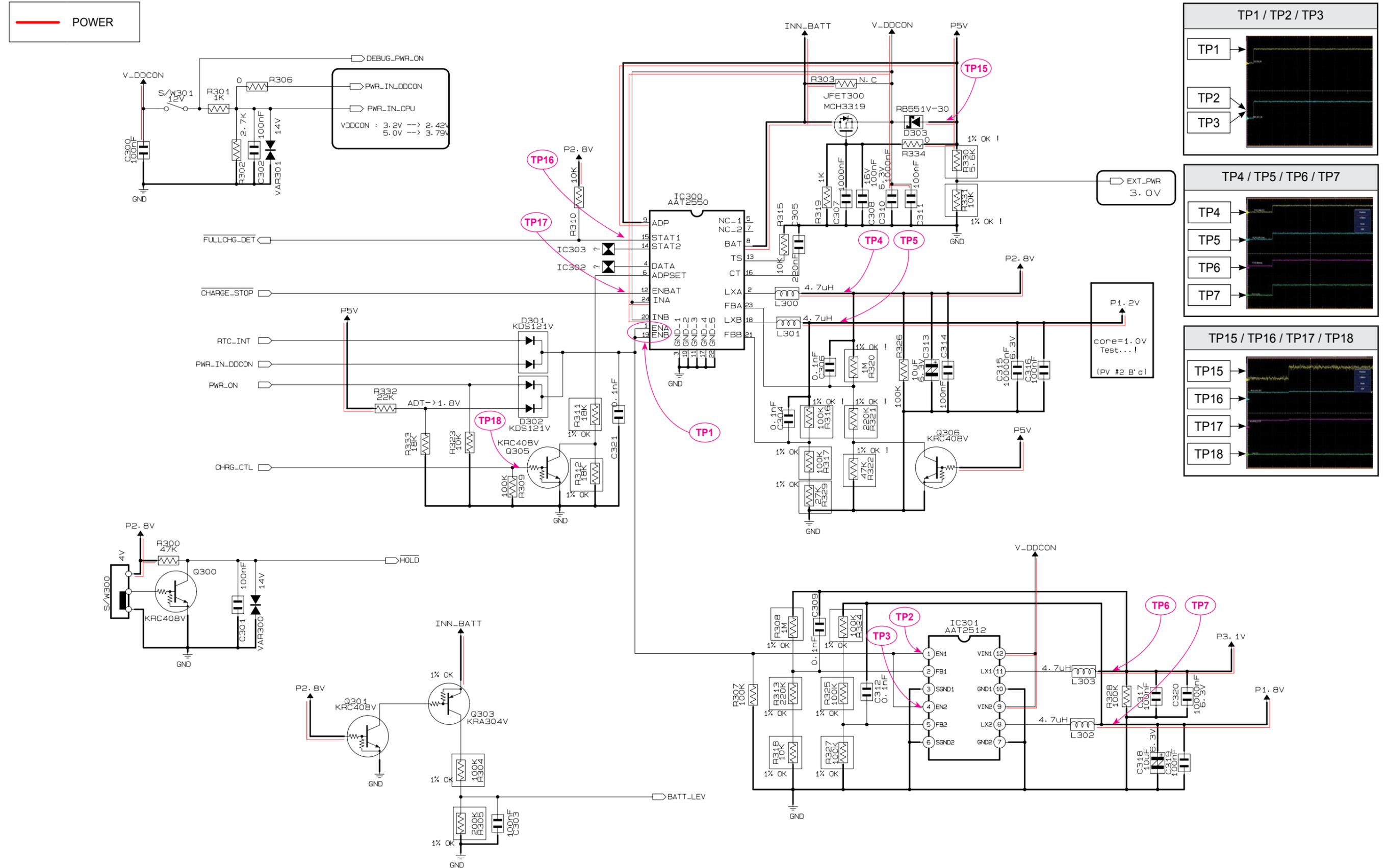
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## 7-1 Overall Block Diagram

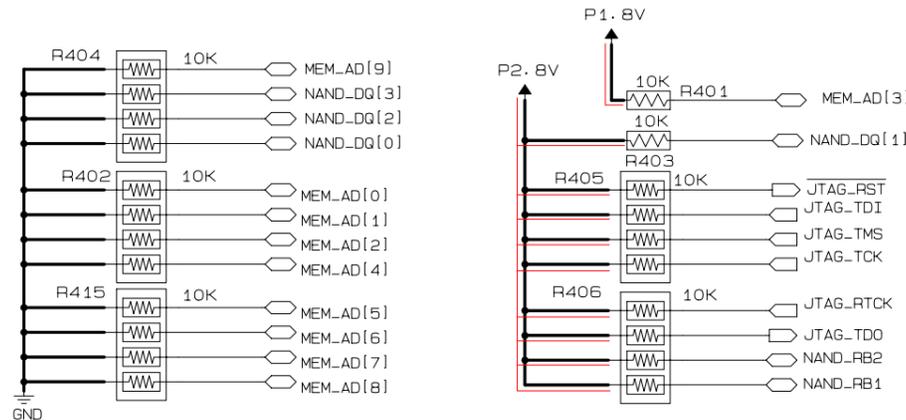
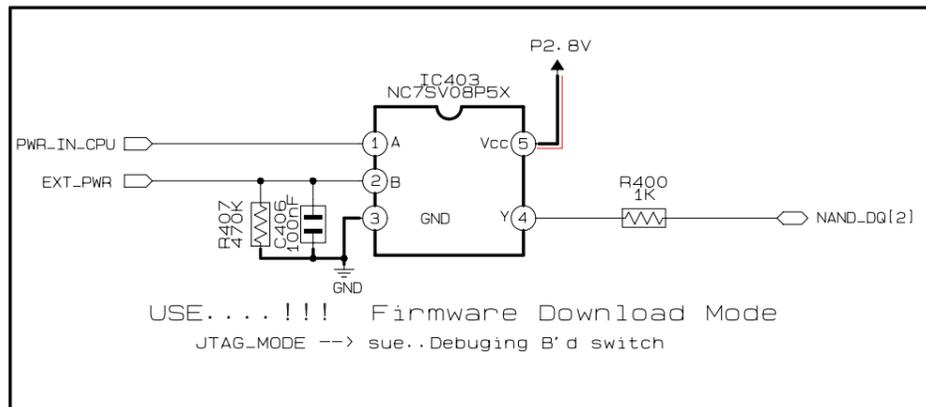
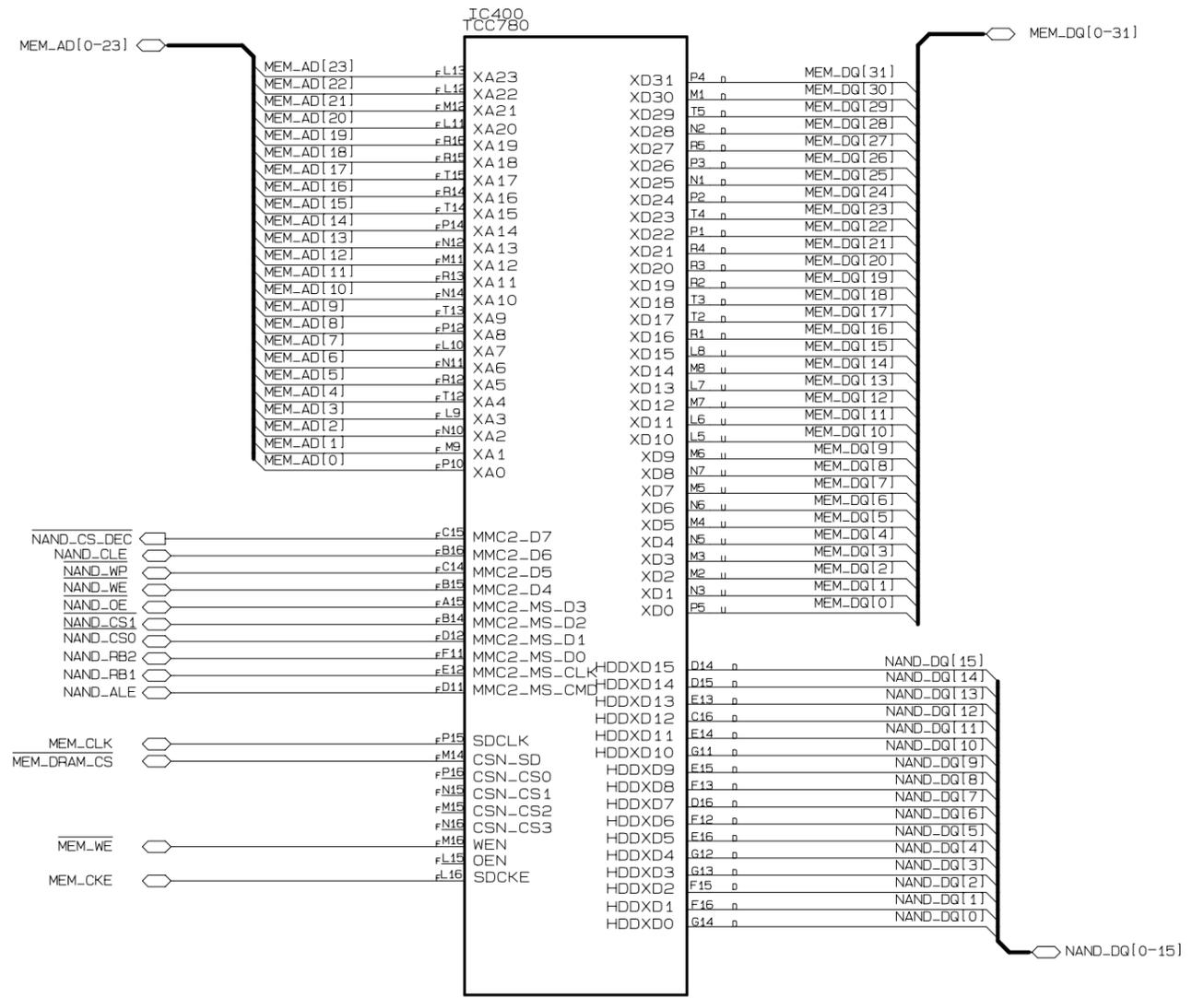
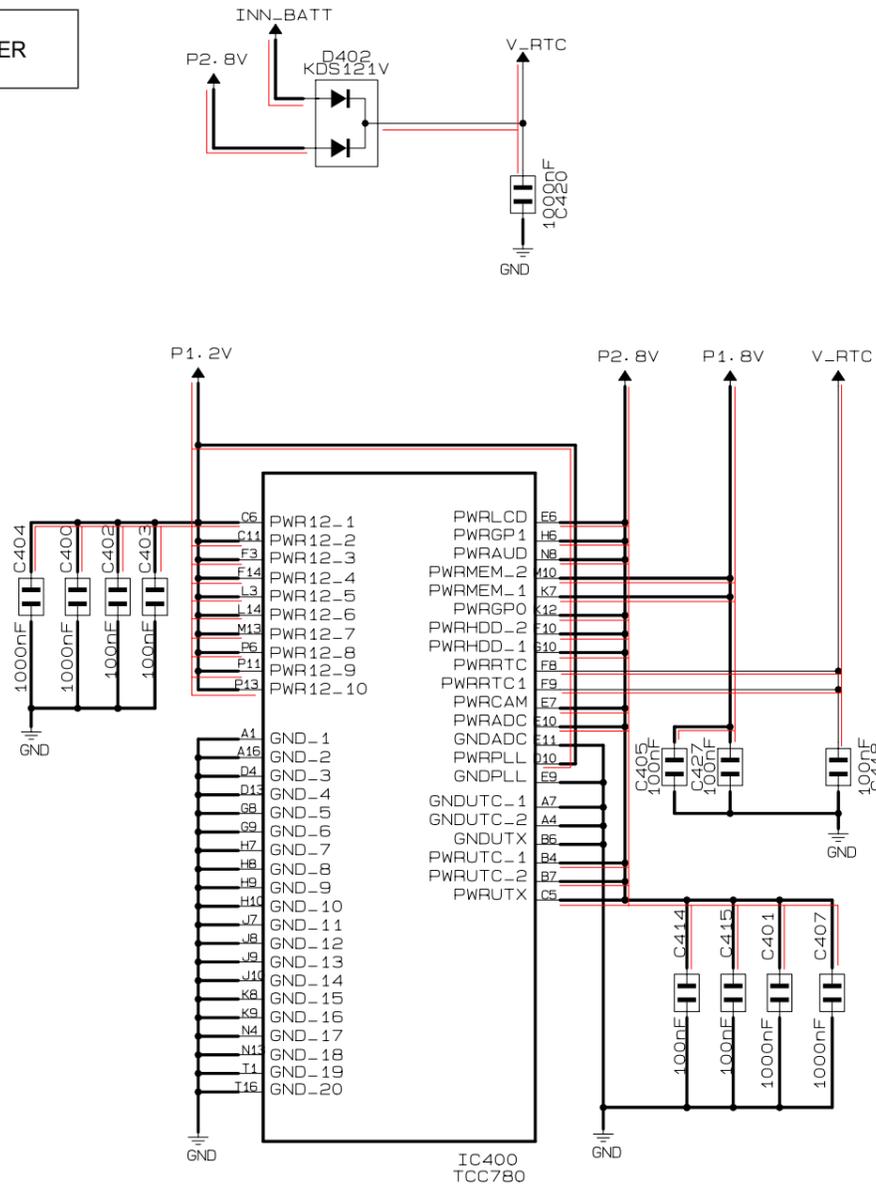


- The Main System consists of such basic circuit blocks as Processor (TCC7801), SDRAM (32Mbit), Flash Memory, Power Block, and Audio DAC, and includes FM\_Tuner, Bluetooth Block, and LCD Block.
- Serial interfaces between the IC's are implemented with various protocols throughout the system. The Host Processor (TCC7801) and the UART are used to implement the Audio DAC and the FM Tuner, the interface between the Bluetooth Module and the Host Process is accomplished by the UART, and the Audio DAC accomplishes mode setting and data transfer through I2C and I2S, respectively.
- Power can be turned on and off with the power switch on the side of the device. Either the built-in battery or the power adapter can be used for power supply. The power adapter is capable of 5V input. The power from the power adapter charges the battery at AAT2550. Four (4) different voltages required are 2.8V (Main), 1.07V (CPU Core), 1.8 (SDRAM Memory), and 3.1V (WQVGA LCD).

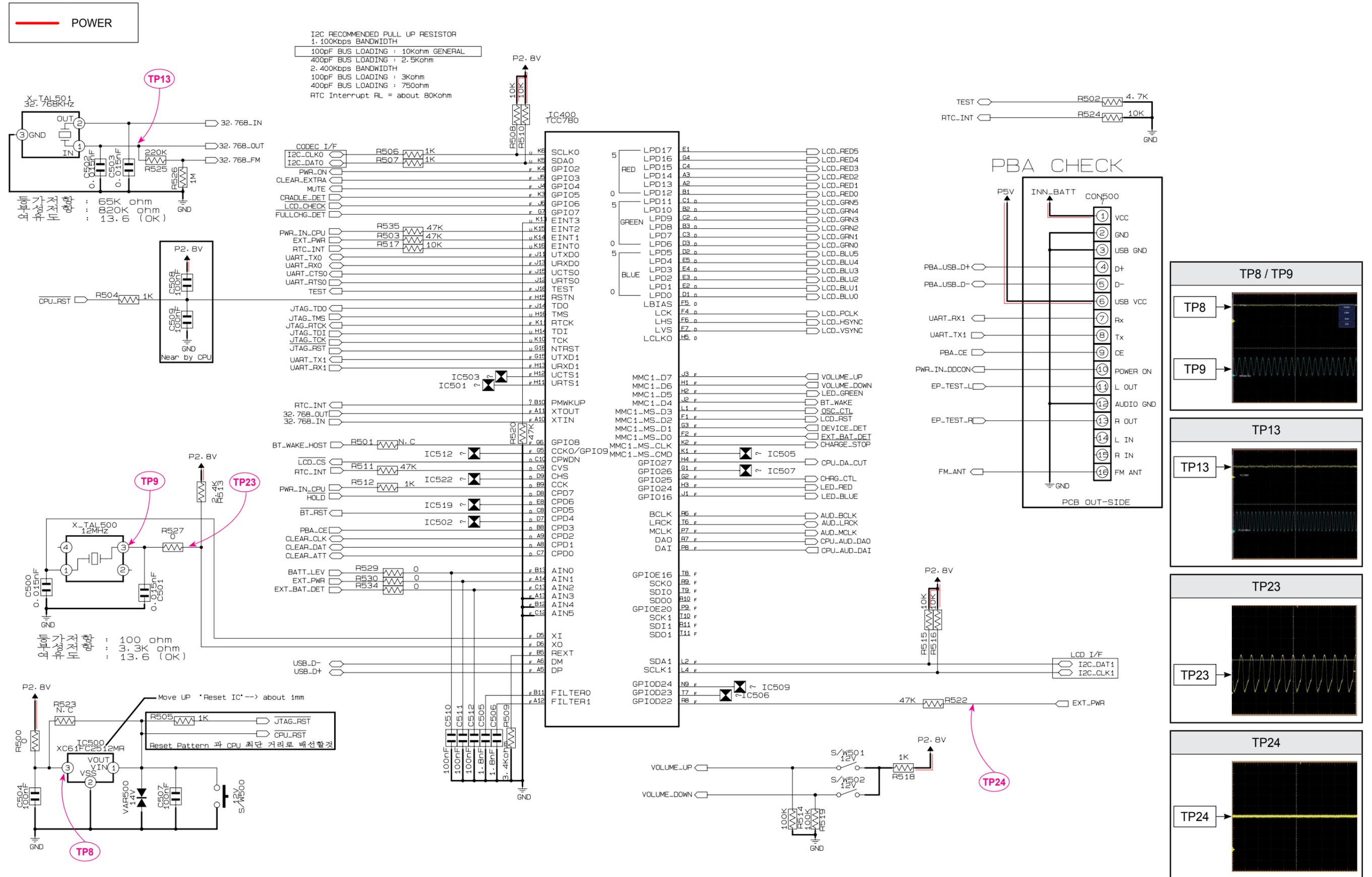
## 7-2 Power Management



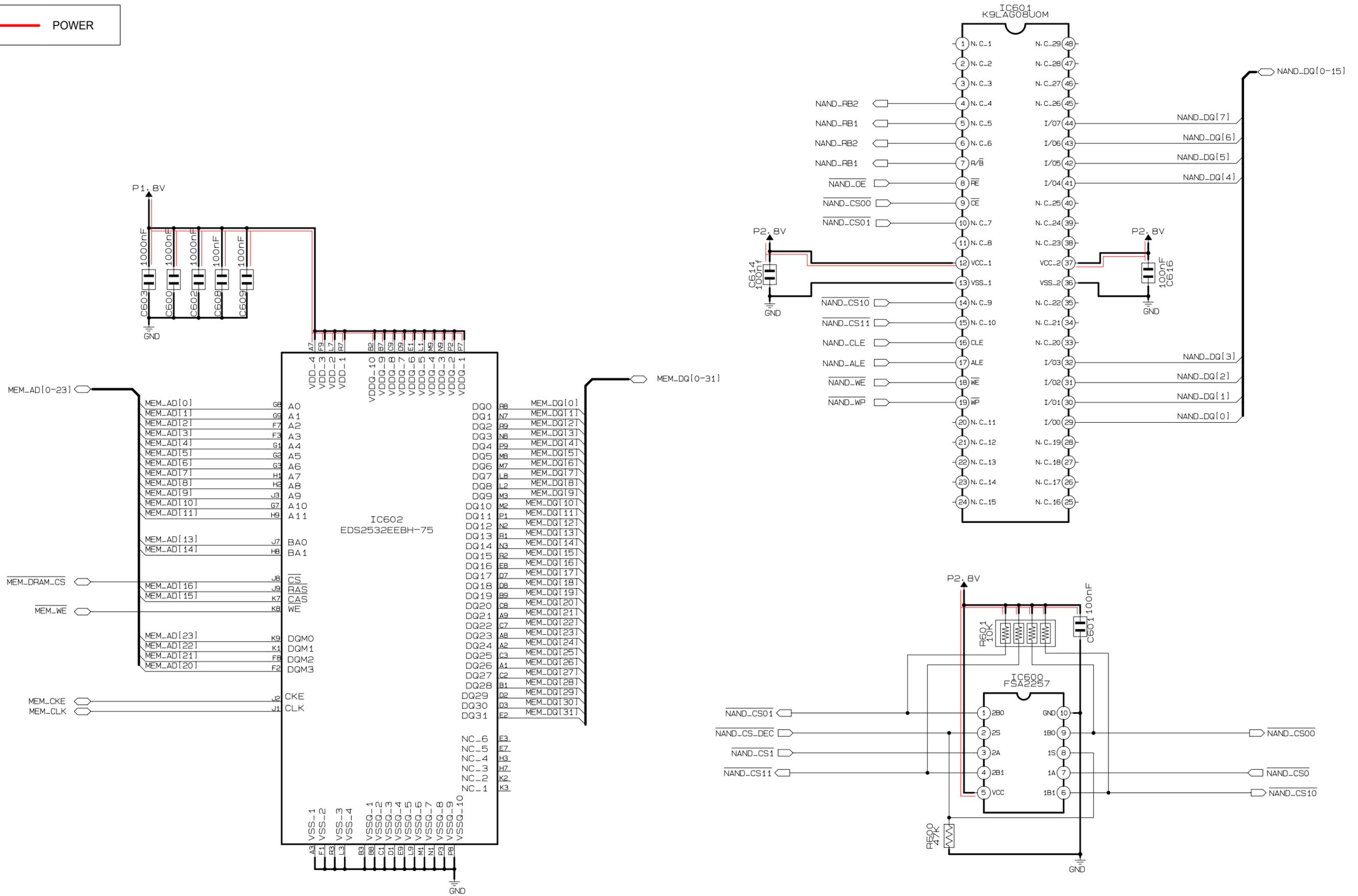
### 7-3 TCC7801 Power\_Block



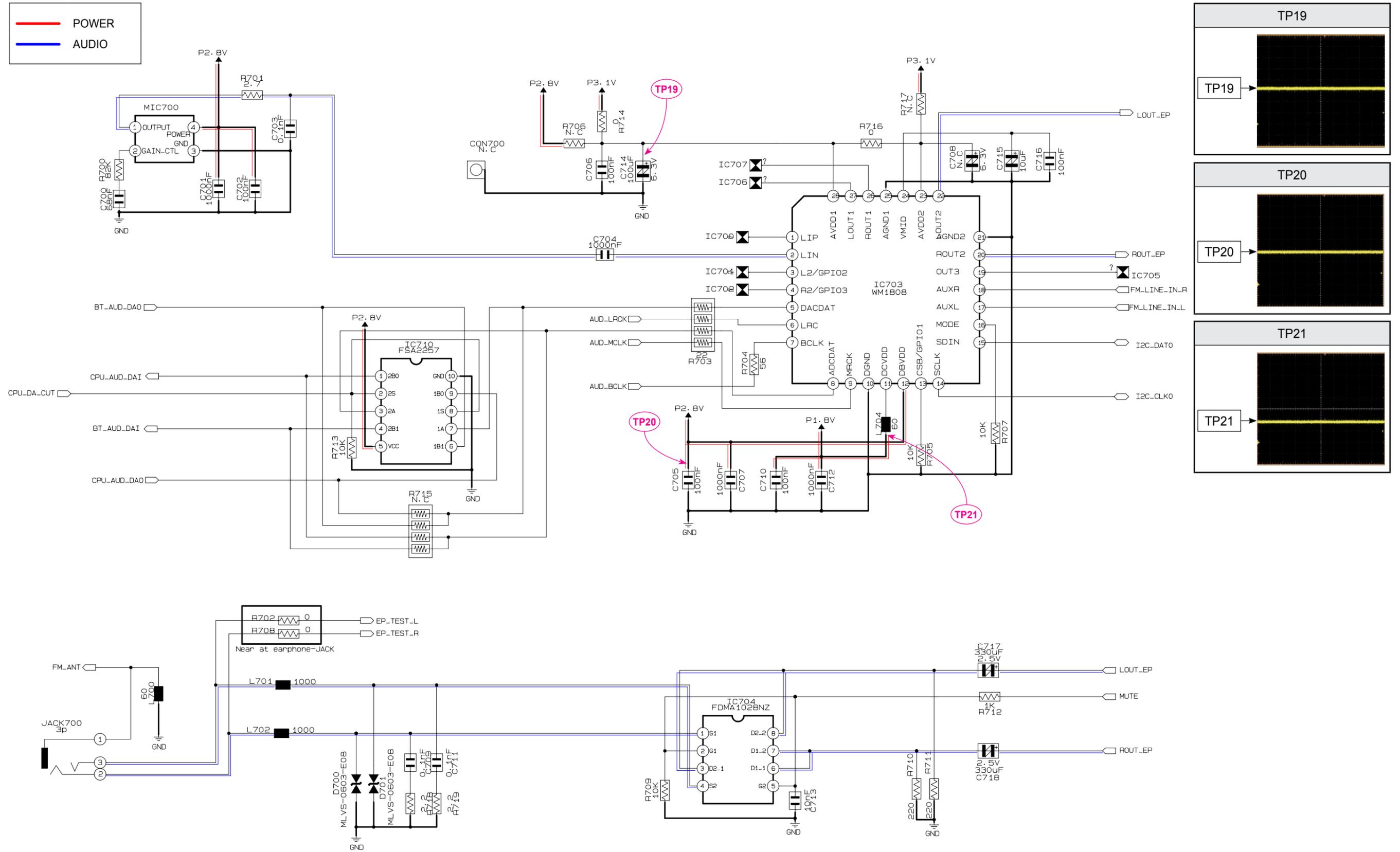
### 7-4 TCC7801 I/O Setting, Reset



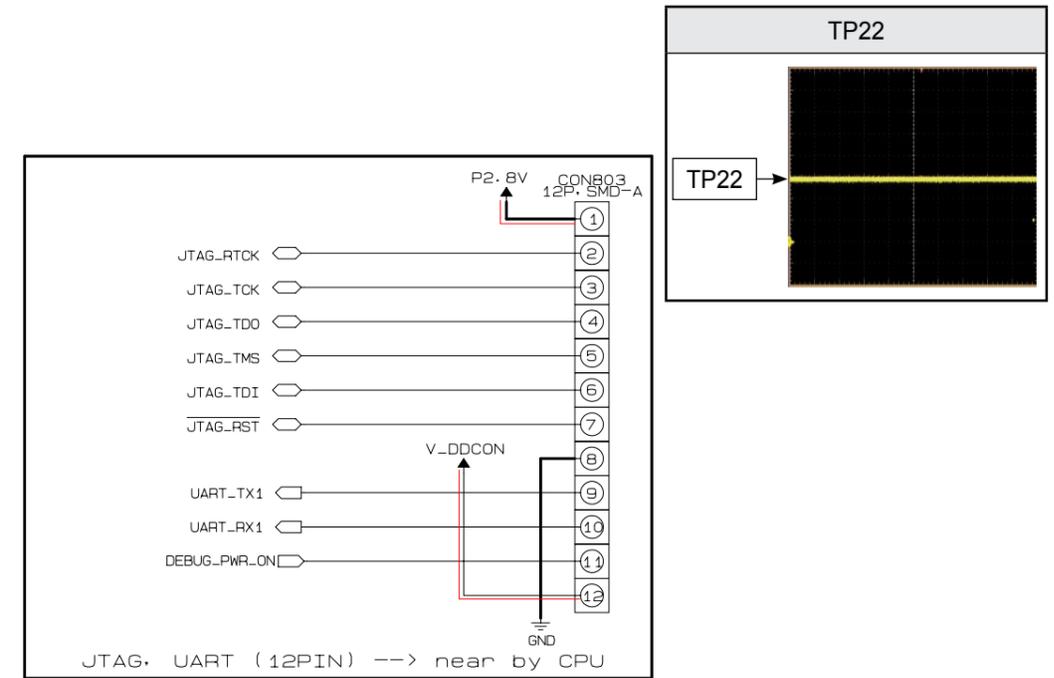
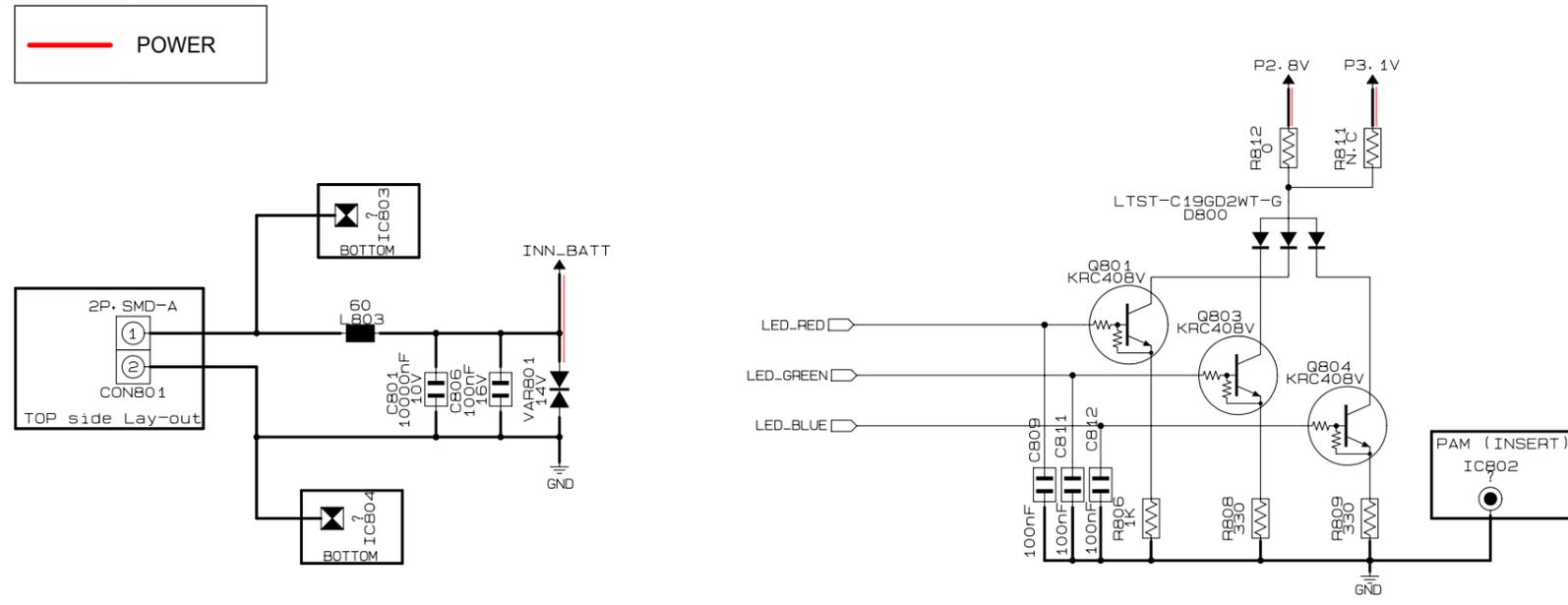
### 7-5 Memory (SDRAM, NAND-Flash)



### 7-6 Audio CODEC

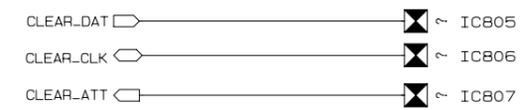
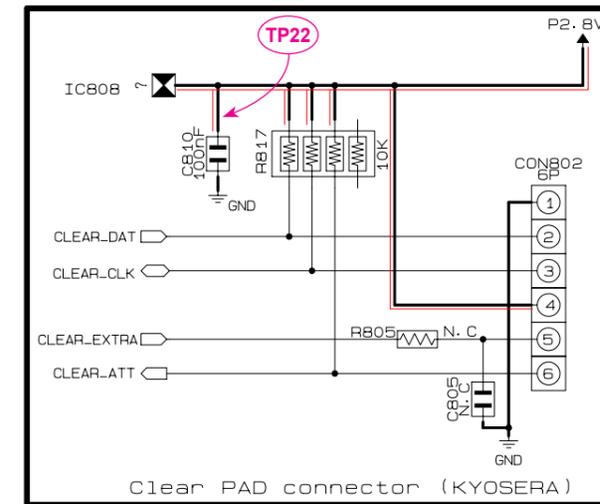
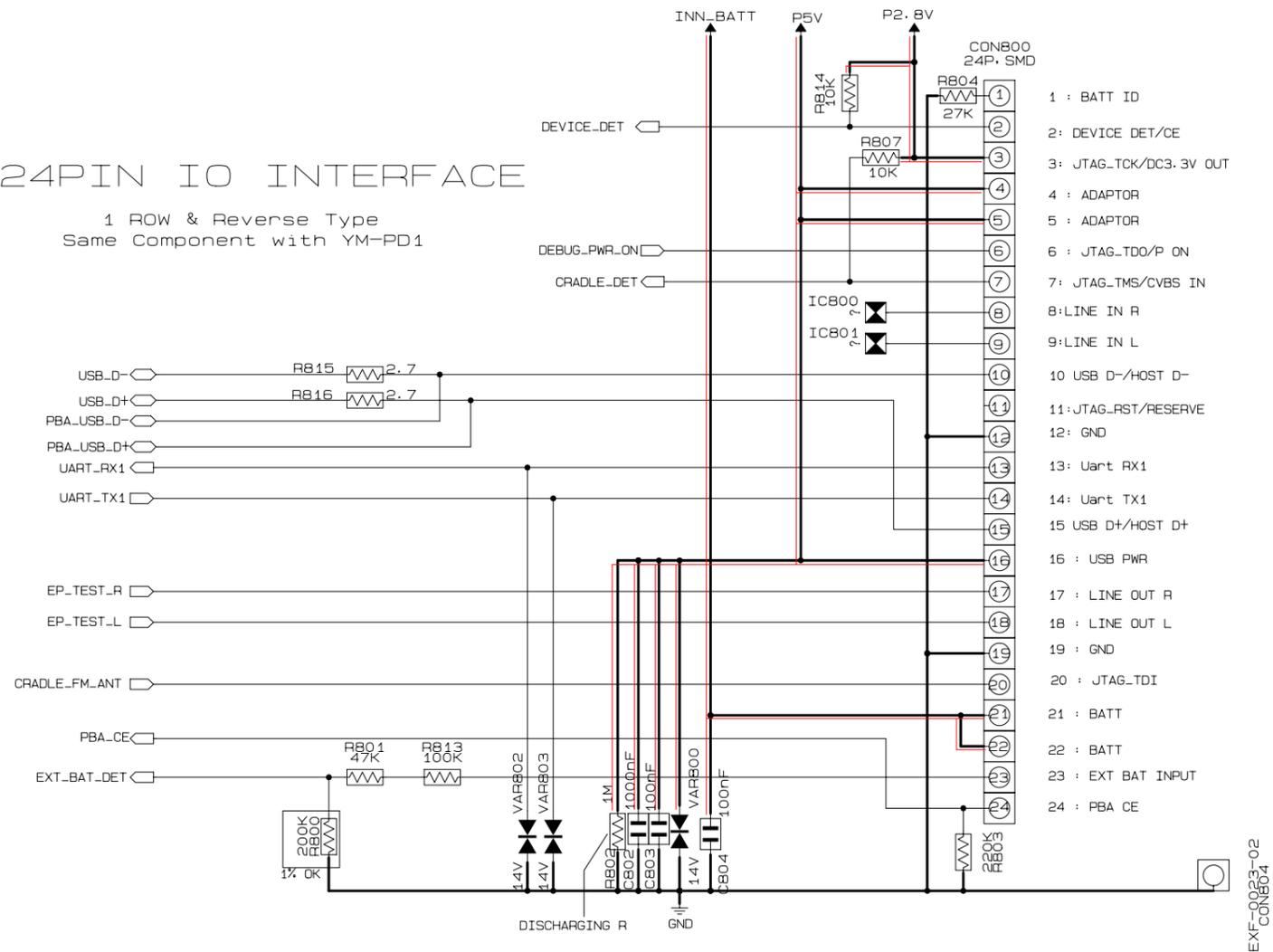


### 7-7 24PIN I/O, Interface



### 24PIN IO INTERFACE

1 ROW & Reverse Type  
Same Component with YM-PD1



### 7-8 WQVGA LCD Interface

